

8961726 TEXAS INSTR (OPTO)

62C 36752 D

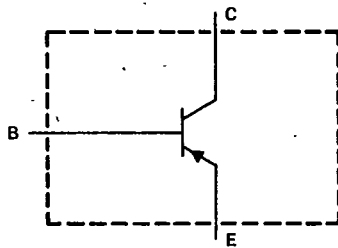
T-33-19

TIP30, TIP30A, TIP30B, TIP30C,  
TIP30D, TIP30E, TIP30F  
P-N-P SILICON POWER TRANSISTORS

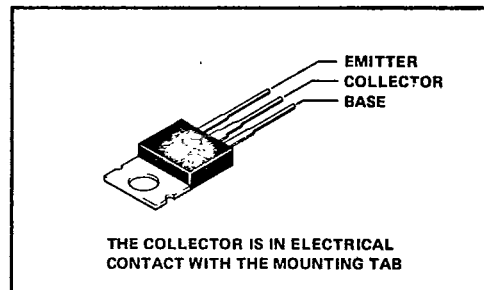
JULY 1968 - REVISED OCTOBER 1984

- Designed for Complementary Use With TIP29 series
- 30 W at 25°C Case Temperature
- 1 A Continuous Collector Current
- 3 A Peak Collector Current
- Minimum  $f_T$  of 3 MHz at 10 V, 0.2 A
- Customer Specified Selections Available
- Designed for Power Amplifier and High-Speed Switching Applications

device schematic



TO-220AB PACKAGE



absolute maximum ratings at 25°C case temperature (unless otherwise noted)

	TIP30	TIP30A	TIP30B	TIP30C
Collector-base voltage	-80 V	-100 V	-120 V	-140 V
Collector-emitter voltage ( $I_B = 0$ )	-40 V	-60 V	-80 V	-100 V
Emitter-base voltage			-5 V	
Continuous collector current			-1 A	
Peak collector current (see Note 1)			-3 A	
Continuous base current			-0.4 A	
Safe operating area at 25°C case temperature			See Figure 4	
Continuous device dissipation at 25°C case temperature (see Note 2)			30 W	
Continuous device dissipation at (or below) 25°C free-air temperature (see Note 3)			2 W	
Unclamped inductive load energy (see Note 4)			32 mJ	
Operating collector junction and storage temperature range			-65°C to 150°C	
Lead temperature 3,2 mm (0.125 inch) from case for 10 seconds			250°C	

- NOTES:
1. This value applies for  $t_W \leq 0.3$  ms, duty cycle  $\leq 10\%$ .
  2. Derate linearly to 150°C case temperature at the rate of 0.24 W/°C.
  3. Derate linearly to 150°C free-air temperature at the rate of 16mW/°C.
  4. This rating is based on the capability of the transistor to operate safely in the circuit in Figure 2.

5  
TIP Devices

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T-33-19

TIP30, TIP30A, TIP30B, TIP30C,  
TIP30D, TIP30E, TIP30F  
P-N-P SILICON POWER TRANSISTORS

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

	TIP30D	TIP30E	TIP30F
Collector-base voltage	-160 V	-180 V	-200 V
Collector-emitter voltage ( $I_B = 0$ )	-120 V	-140 V	-160 V
Emitter-base voltage	-5 V		
Continuous collector current	-1 A		
Peak collector current (see Note 1)	-3 A		
Continuous base current	-0.4 A		
Safe operating area at 25°C case temperature	See Figure 4		
Continuous device dissipation at 25°C case temperature (see Note 2)	30 W		
Continuous device dissipation at (or below) 25°C free-air temperature (see Note 3)	2 W		
Unclamped inductive load energy (see Note 4)	32 mJ		
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- NOTES: 1. This value applies for  $t_W \leq 0.3$  ms, duty cycle  $\leq 10\%$ .  
 2. Derate linearly to 150°C case temperature at the rates of 0.24 W/°C.  
 3. Derate linearly to 150°C free-air temperature at the rate of 16 mW/°C.  
 4. This rating is based on the capability of the transistor to operate safely in the circuit in Figure 2.

electrical characteristics at 25°C case temperature

PARAMETER	TEST CONDITIONS	TIP30		TIP30A		TIP30B		TIP30C		UNIT	
		MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP		MAX
$V_{(BR)CEO}$	$I_C = -30$ mA, $I_B = 0$ , See Note 5	-40			-60			-80		-100	V
$I_{CEO}$	$V_{CE} = -30$ V, $I_B = 0$		-0.3			-0.3					mA
	$V_{CE} = -60$ V, $I_B = 0$						-0.3		-0.3		
$I_{CES}$	$V_{CE} = -80$ V, $V_{BE} = 0$		-0.2								mA
	$V_{CE} = -100$ V, $V_{BE} = 0$				-0.2						
	$V_{CE} = -120$ V, $V_{BE} = 0$						-0.2				
$I_{EBO}$	$V_{CE} = -140$ V, $V_{BE} = 0$						-0.2		-0.2		mA
	$V_{EB} = -5$ V, $I_C = 0$		-1		-1		-1		-1		
$h_{FE}$	$V_{CE} = -4$ V, $I_C = -0.2$ A, See Notes 5 and 6	20			20			20		20	
	$V_{CE} = -4$ V, $I_C = -1$ A, See Notes 5 and 6	15	75		15	75		15	75	15	
$V_{BE}$	$V_{CE} = -4$ V, $I_C = -1$ A, See Notes 5 and 6		-1.3		-1.3			-1.3		-1.3	V
$V_{CE(sat)}$	$I_B = -125$ mA, $I_C = -1$ A, See Notes 5 and 6		-0.7		-0.7			-0.7		-0.7	V
$h_{fe}$	$V_{CE} = -10$ V, $I_C = -0.2$ A, $f = 1$ kHz	20			20			20		20	
$ h_{fe} $	$V_{CE} = -10$ V, $I_C = -0.2$ A, $f = 1$ MHz	3			3			3		3	

- NOTES 5. These parameters must be measured using pulse techniques,  $t_W = 300 \mu s$ , duty cycle  $\leq 2\%$ .  
 6. These parameters are measured using voltage-sensing contacts separate from the current-carrying contacts.

TIP Devices

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TIP30, TIP30A, TIP30B, TIP30C,  
TIP30D, TIP30E, TIP30F  
P-N-P SILICON POWER TRANSISTORS

electrical characteristics at 25°C case temperature

T-33-19

PARAMETER	TEST CONDITIONS	TIP30D			TIP30E			TIP30F			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
V <sub>(BR)CEO</sub>	I <sub>C</sub> = -30 mA, I <sub>B</sub> = 0, See Note 5	-120			-140			-160			V
I <sub>CEO</sub>	V <sub>CE</sub> = -90 V, I <sub>B</sub> = 0		-0.3			-0.3			-0.3		mA
I <sub>CES</sub>	V <sub>CE</sub> = -90 V, V <sub>BE</sub> = 0		-0.2								mA
	V <sub>CE</sub> = -180 V, V <sub>BE</sub> = 0					-0.2					
	V <sub>CE</sub> = -200 V, V <sub>BE</sub> = 0								-0.2		
I <sub>EBO</sub>	V <sub>EB</sub> = -5 V, I <sub>C</sub> = 0		-1			-1				-1	mA
h <sub>FE</sub>	V <sub>CE</sub> = -4 V, I <sub>C</sub> = -0.2 A, See Notes 5 and 6	40			40			40			
	V <sub>CE</sub> = -4 V, I <sub>C</sub> = -1 A, See Notes 5 and 6	15			15			15			
V <sub>BE</sub>	V <sub>CE</sub> = -4 V, I <sub>C</sub> = -1 A, See Notes 5 and 6		-1.3			-1.3			-1.3		V
V <sub>CE(sat)</sub>	I <sub>B</sub> = -125 mA, I <sub>C</sub> = -1 A, See Notes 5 and 6		-0.7			-0.7			-0.7		V
h <sub>fe</sub>	V <sub>CE</sub> = -10 V, I <sub>C</sub> = -0.2 A, f = 1 kHz	20			20			20			
h <sub>fe1</sub>	V <sub>CE</sub> = -10 V, I <sub>C</sub> = -0.2 A, f = 1 MHz	3			3						

NOTES: 5. These parameters must be measured using pulse techniques, t<sub>w</sub> = 300 μs, duty cycle ≤ 2%.  
6. These parameters are measured with voltage-sensing contact separate from the current-carrying contacts.

thermal characteristics

PARAMETER	MIN	TYP	MAX	UNIT
R <sub>θJC</sub>		4.17		°C/W
R <sub>θJA</sub>		62.5		

resistive-load switching characteristics at 25°C case temperature

PARAMETER	TEST CONDITIONS†			MIN	TYP	MAX	UNIT
t <sub>on</sub>	I <sub>C</sub> = -1 A, I <sub>B1</sub> = -0.1 A, I <sub>B2</sub> = 0.1 A,				0.3		μs
t <sub>off</sub>	V <sub>BE(off)</sub> = 4.3 V, R <sub>L</sub> = 30 Ω, See Figure 1				1		

† Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.



TIP Devices

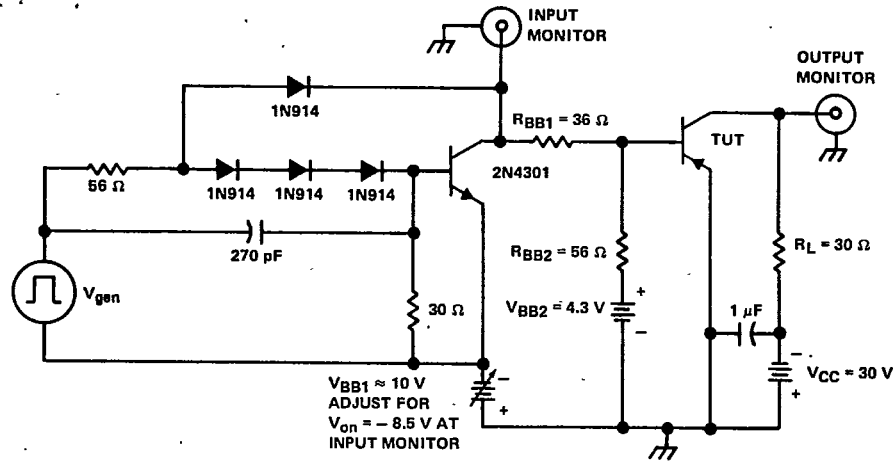
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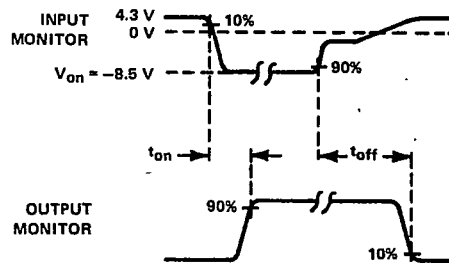
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P-N-P SILICON POWER TRANSISTORS

PARAMETER MEASUREMENT INFORMATION



TEST CIRCUIT



VOLTAGE WAVEFORMS

- NOTES: A.  $V_{gen}$  is a 30-V pulse into a 50  $\Omega$  termination.  
 B. The  $V_{gen}$  waveform is supplied by the following characteristics:  $t_r \leq 15$  ns,  $t_f \leq 15$  ns,  $Z_{out} = 50 \Omega$ ,  $t_w = 20 \mu$ s, duty cycle  $\leq 2\%$ .  
 C. Waveforms are monitored on an oscilloscope with the following characteristics:  $t_r \leq 15$  ns,  $R_{in} \geq 10$  M $\Omega$ ,  $C_{in} \leq 11.5$  pF.  
 D. Resistors must be noninductive types.  
 E. The d-c power supplies may require additional bypassing in order to minimize ringing.

FIGURE 1. RESISTIVE-LOAD SWITCHING

TIP Devices

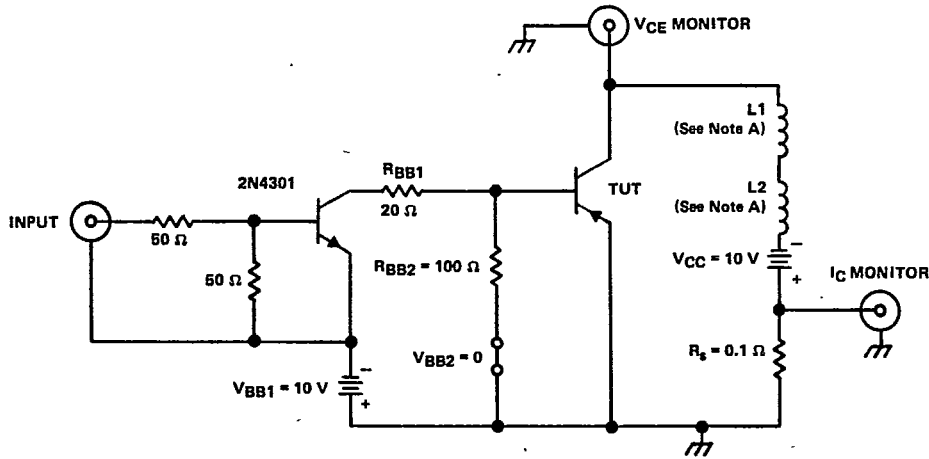
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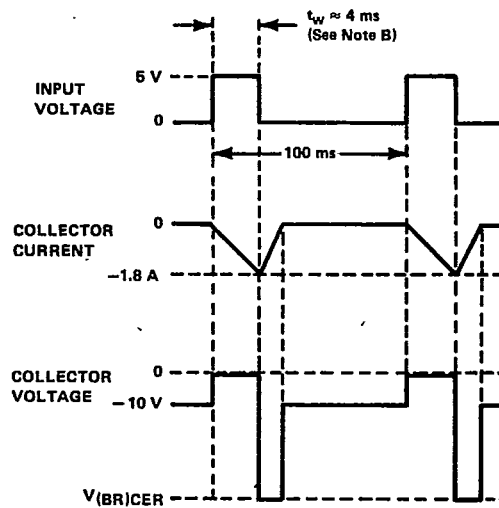
TIP30, TIP30A, TIP30B, TIP30C,  
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P-N-P SILICON POWER TRANSISTORS

PARAMETER MEASUREMENT INFORMATION

T-33-19



TEST CIRCUIT



VOLTAGE AND CURRENT WAVEFORMS

NOTES: A. L1 and L2 are 10 mH, 0.11 Ω, Chicago Standard Transformer Corporation C-2688, or equivalent.  
 B. Input pulse duration is increased until  $I_{CM} = -1.8$  A

FIGURE 2. INDUCTIVE-LOAD SWITCHING

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T-33-19

TYPICAL CHARACTERISTICS

STATIC FORWARD CURRENT TRANSFER RATIO  
VS  
COLLECTOR CURRENT

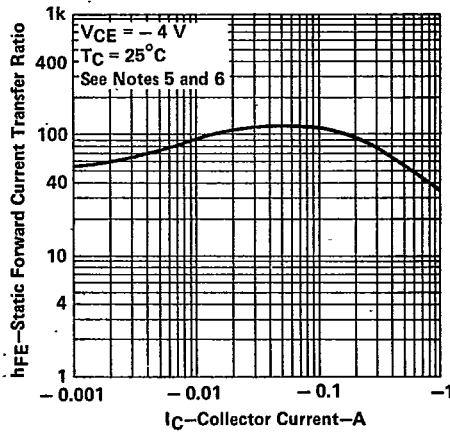


FIGURE 3

- NOTES: 5. These parameters must be measured using pulse techniques,  $t_w = 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .  
6. These parameters are measured with voltage-sensing contacts separate from the current-carrying contacts.

MAXIMUM SAFE OPERATING AREA

FORWARD-BIAS SAFE OPERATING AREA

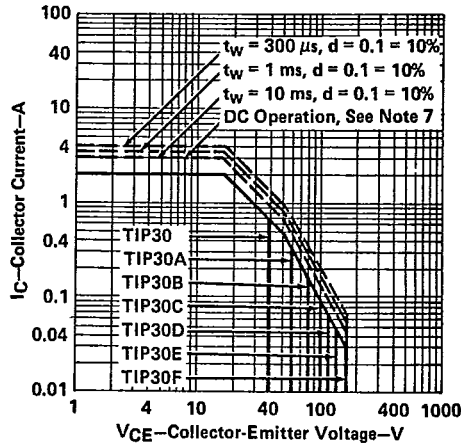


FIGURE 4

NOTE 7: This combination of maximum voltage and current values may be achieved only when switching from saturation to cutoff with a clamped inductive load.



TIP Devices

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THERMAL INFORMATION

T-33-19

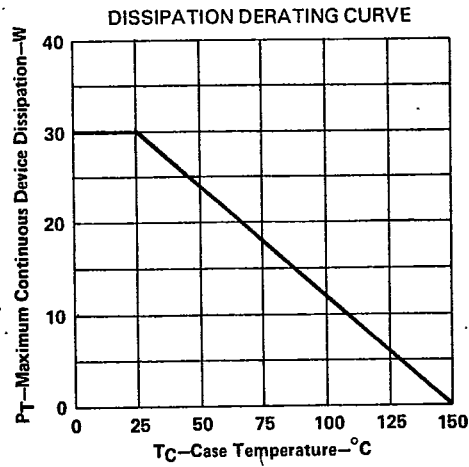


FIGURE 5



TIP Devices