

TENTATIVE DATA

R.F. GERMANIUM ALLOY-DIFFUSED TRANSISTOR

Germanium alloy-diffused transistor of the p-n-p type in a metal case with low noise and high gain up to 260 Mc/s, for use in V.H.F. applications as amplifier-, oscillator- and converter circuits.

LIMITING VALUES (Absolute max. values)

Collector

Voltage (base reference)	$-V_{CB} = \text{max.}$	25 V
Current	$-I_C = \text{max.}$	10 mA

Emitter

Reverse current	$-I_E = \text{max.}$	1 mA
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Base

Current	$-I_B = \text{max.}$	1 mA
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Dissipation

Total dissipation	$P_{\text{tot}} = \text{max.}$	110 mW
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Temperatures

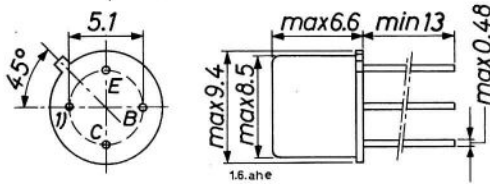
Storage temperature	$T_S = -55 \text{ }^\circ\text{C to } +75 \text{ }^\circ\text{C}$
Junction temperature continuous	$T_j = \text{max. } \overset{75}{90} \text{ }^\circ\text{C}$
incidentally (total dura- tion max. 200 hrs)	$T_j = \text{max. } \overset{90}{90} \text{ }^\circ\text{C}$
	$(t = \text{max. } 200 \text{ hrs})$

THERMAL DATA

Thermal resistance from junction to ambience in free air	$K = \text{max. } 0.4 \text{ }^\circ\text{C/mW}$
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~~Shield lead~~

Dimensions in mm
TO-12 case



CHARACTERISTICS at $T_{amb} = 25\text{ }^{\circ}\text{C}$

Collector current at $I_E = 0$

$-V_{CB} = 12\text{ V}$	$-I_{CBO} < 10\text{ }\mu\text{A}$
$-V_{CB} = 25\text{ V}$	$-I_{CBO} < 50\text{ }\mu\text{A}$

Emitter voltage at $I_C = 0$

$-I_E = 50\text{ }\mu\text{A}$	$-V_{EB} > 0.5\text{ V}$
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Base current

$-V_{CB} = 12\text{ V}; -I_C = 1\text{ mA}$	$-I_B < 50\text{ }\mu\text{A}$
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Base voltage

$-V_{CB} = 12\text{ V}; -I_C = 1\text{ mA}$	$-V_{BE} > 220\text{ mV}$
	$-V_{BE} < 360\text{ mV}$

CHARACTERISTICS RANGE VALUES FOR EQUIP-
MENT DESIGN

$T_{amb} = 25\text{ }^{\circ}\text{C}$

Frequency at which $|h_{fe}| = 1$

$-V_{CB} = 12\text{ V}; I_E = 1\text{ mA}$	$f_1 = 180\text{ Mc/s}$
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Base impedance

$-V_{CB} = 12\text{ V}; I_E = 1\text{ mA}$ $f = 2\text{ Mc/s}$	$ z_{rb} = 10\text{ }\Omega$
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Feedback capacitance

$-V_{CE} = 12\text{ V}; -I_C = 1\text{ mA}$ $f = 0.45\text{ Mc/s}$	$-c_{re} = 0.8\text{ pF}$
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1) Shield lead

CHARACTERISTICS RANGE VALUES FOR EQUIP-
MENT DESIGN (continued)

Current amplification factor

$$-V_{CE} = 12 \text{ V}; -I_C = 1 \text{ mA}$$

$$f = 1 \text{ kc/s}$$

$$h_{fe} > 20$$

Noise figure

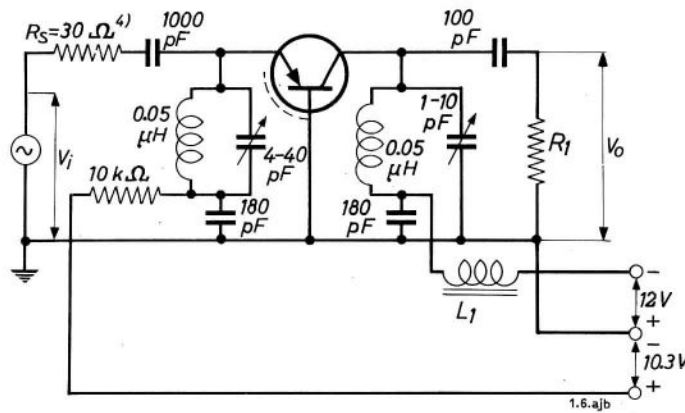
$$-V_{CE} = 12 \text{ V}; -I_C = 1 \text{ mA}$$

$$f = 200 \text{ Mc/s}$$

$$F = 6 \text{ dB} < 7.5 \text{ dB}$$

Input source resistance
= 30Ω

Test circuit for power gain at 200 Mc/s



R_L is chosen such that the total impedance R_L of the tuned circuit is $2.0 \text{ k}\Omega$.

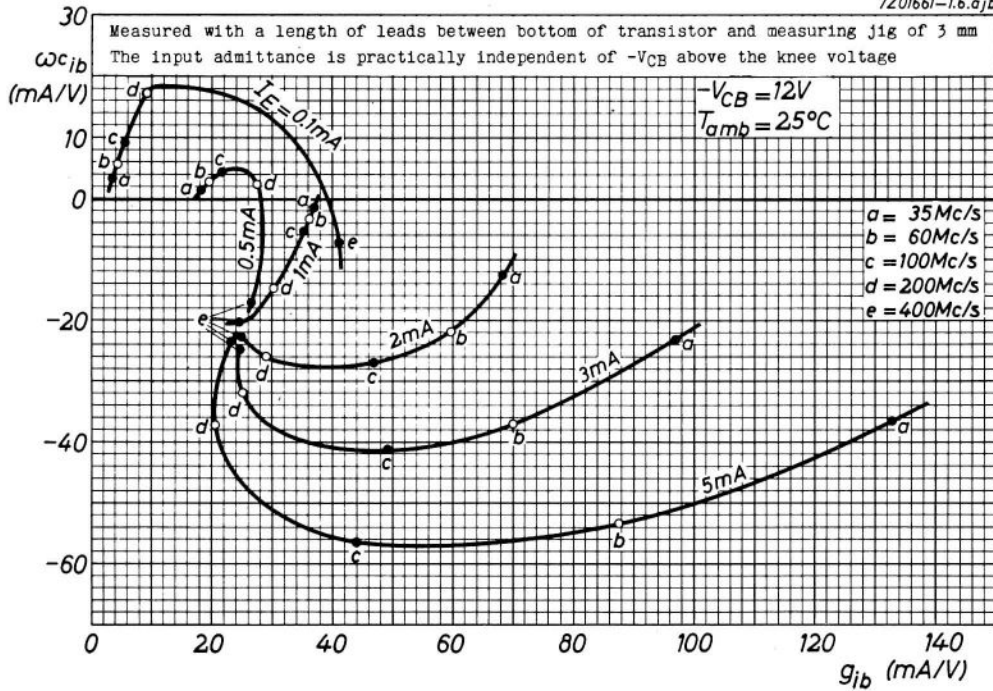
L_1 = ferrite bead

Available power gain at 200 Mc/s in the circuit above

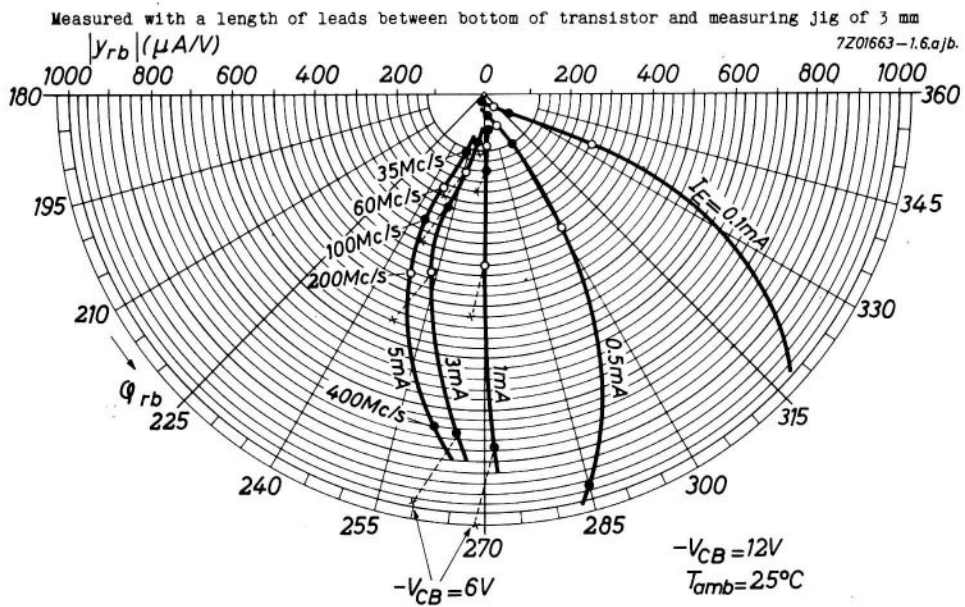
$$\text{At } f = 100 \text{ Mc/s} \quad G = 13 \text{ dB} > 10 \text{ dB}$$

The available power gain is defined as

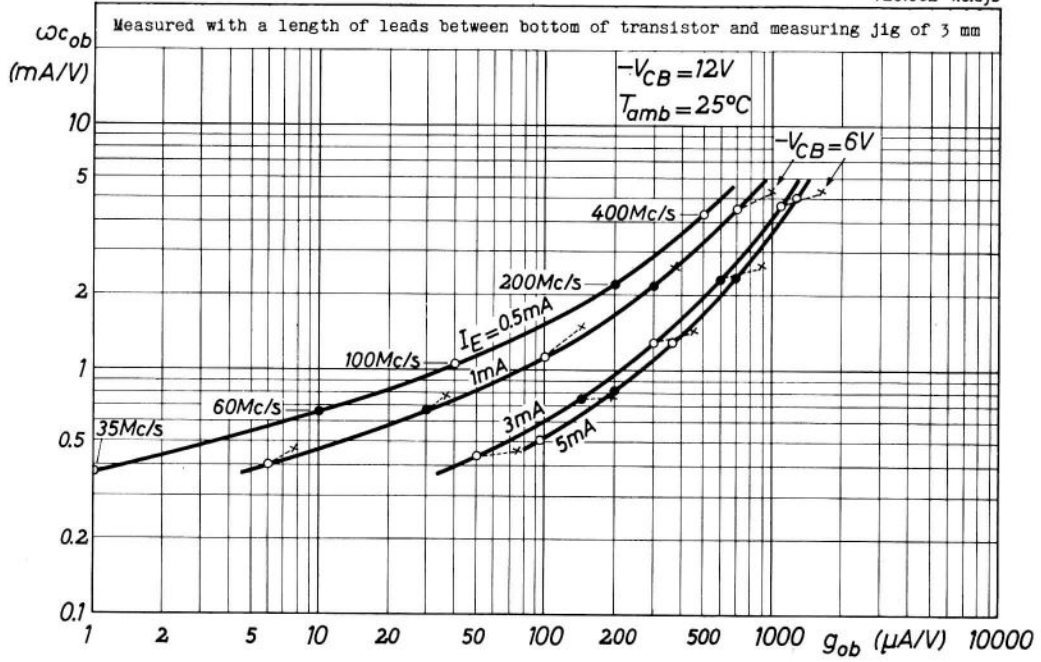
$$G = \frac{V_o^2}{V_i^2} \cdot \frac{4R_s}{R_L} = 0.073 \frac{V_o^2}{V_i^2}$$



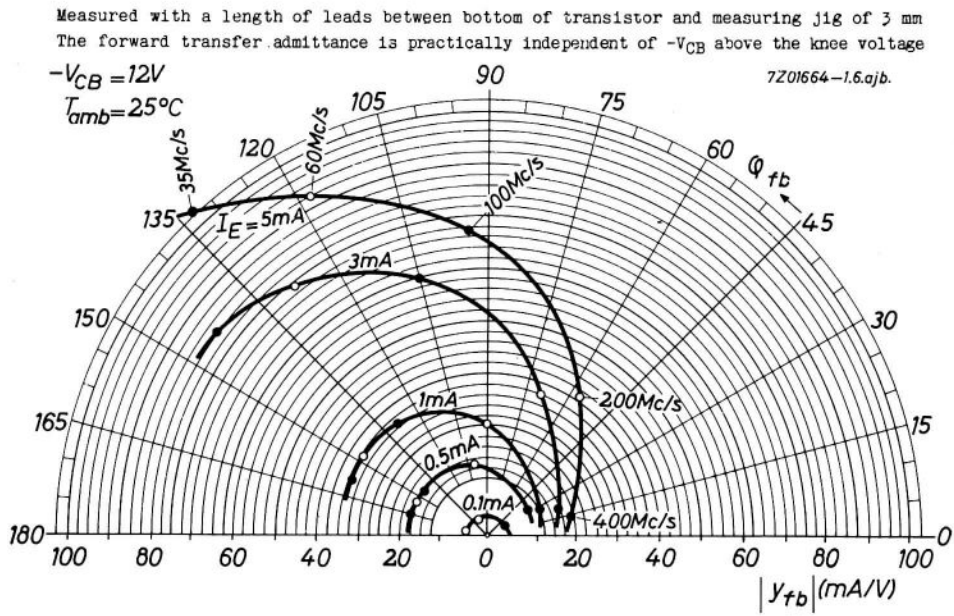
Typical input admittance in common base circuit



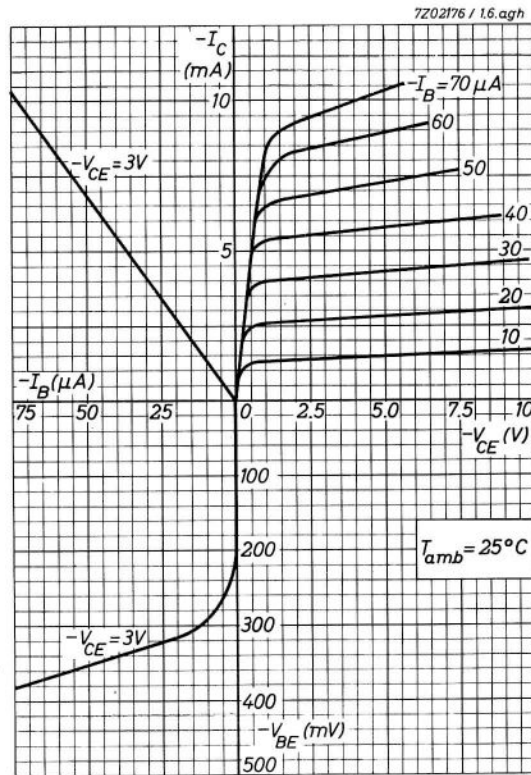
Typical feedback admittance in common base circuit



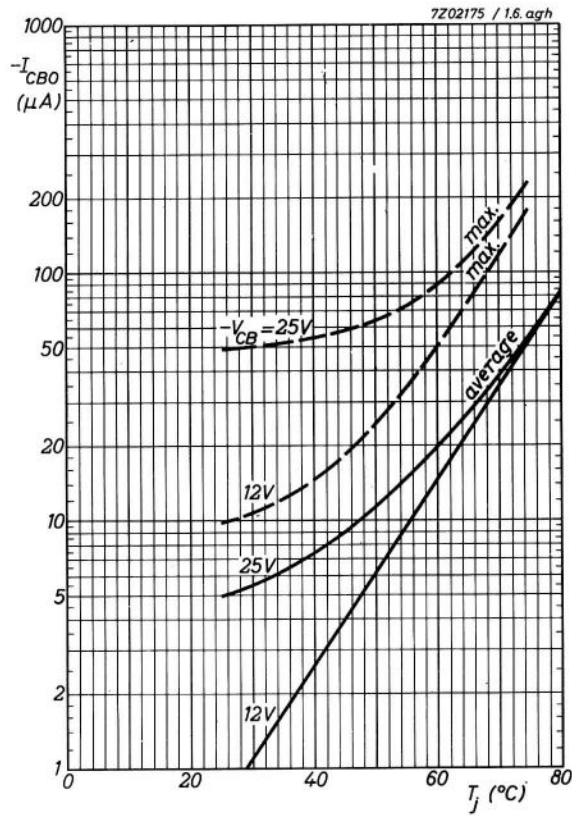
Typical output admittance in common base circuit



Typical forward transfer admittance in common base circuit



Typical characteristics



Collector leakage current as a function of the junction temperature